**Appendix B: Switch & Jumper Settings**

- **Boot ROM enable**: Switch 4 in the primary SC5I device and a boot ROM is present.
- **Zero Wait State Operation**: Switch 2 in the primary SC5I device.
- **Full Handshake Interrupt SW3**: Switch S1-1 on the Roon drive is in the ON position.
- **ON**: Switch SW4 in the primary SC5I device.
- **OFF**: Switch SW4 in the primary SC5I device.

**Handshake Interrupts**

- **Disabled**: Switch SW2 in the primary SC5I device.
- **Embedded**: Switch SW2 in the primary SC5I device.

**Full Handshake Method**

- **Disabled**: Switch SW3 in the primary SC5I device.
- **Embedded**: Switch SW3 in the primary SC5I device.

**Memory-mapped address SW7**

- **0x0000**: Switch S1-1 in the primary SC5I device.
- **0x0080**: Switch S1-1 in the primary SC5I device.
- **0x00C0**: Switch S1-1 in the primary SC5I device.
- **0x0080**: Switch S1-1 in the primary SC5I device.

**Card Address**

- **0x0000**: Switch S1-1 in the primary SC5I device.
- **0x0080**: Switch S1-1 in the primary SC5I device.
- **0x00C0**: Switch S1-1 in the primary SC5I device.
- **0x0080**: Switch S1-1 in the primary SC5I device.

**Onboard VGA Card**

- **Disabled**: Switch S1-1 in the primary SC5I device.
- **Enabled**: Switch S1-1 in the primary SC5I device.

**Note**: The 1.128P will work with one of your memory-mapped addresses.
Figure 16: T128F Switch and Jumper Location

Interrupt Jumper Block J5

Note that all interrupts are enabled, section of IRQ10, 12, 14 or 15

Under MS-DOS, leave all jumpers off.

This jumper block is used for interrupt section under the Novel and

Reserved Switches

Switches 1 and 2 are reserved and should remain in the OFF position at